

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Johannes Becker  
Serial No.: 10/706,365  
Filed: November 12, 2003  
Title: SYSTEM AND METHOD FOR SECURING AN INTEGRATED CIRCUIT  
AS AGAINST SUBSEQUENT REPROGRAMMING  
Grp./A.U.: 2135  
Examiner: Suman Debnath Confirmation No.: 6816

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically filed  
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June 4, 2007 (Date)  
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/Debbie Sams/  
(Signature of the person signing the certificate)

Sir:

**REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111**

The Applicant has carefully considered this application in connection with the non-final Examiner's Action electronically delivered March 2, 2007, and respectfully requests reconsideration of this application in view of the following remarks.